

Ref: Tender No. C-DAC/EOI/FPGA Cluster/11-12  
Dated: 22.07.2011

**Expression of Interest [ EOI ] to provide a FPGA cluster based on Xilinx  
Virtex6 LX550T / Equivalent FPGA at C-DAC Knowledge Park,  
Bangalore**

Queries raised by the bidders and the clarification are as follows:

S. No.	Query	Clarification
1.	<p><b>M/s. Picosem Technologies</b></p> <ul style="list-style-type: none"><li>• We are a new start up company incorporated in the fiscal year 2010, predominately focused on FPGA and COTs FPGA boards. We have become trusted partners for our principles Viz Picocomputing, 4DSP and Kaisemi and engaged dedicated teams for marketing and support for our principles. We are able to penetrate and educate vast number of customers in India about these products. Presently we are don't have balance sheet available for submission along with other EOI documents? We would like to know from you, is balance sheet mandatory for expression of interest?</li><li>• We request you to provide a brief on payment terms as well? Will you accept bank guarantee for the warranty term payments?</li></ul>	<p>Audited accounts of the previous three financial years i.e., 2010-11, 2009-10 and 2008-07 with audited statements / annual reports to be submitted along with the EOI documents.</p> <p>Bank Guarantee is acceptable for the warranty period for an equivalent amount. The details shall be provided in the RFQ documents.</p>