

Centre for Development of Advanced Computing (C-DAC)

(A Scientific Society under Ministry of Communications & IT, Govt. of India)
CDAC Knowledge Park, No.1, Old Madras Road, Byappanahalli, Bangalore-560038
Ph: 080-66116400/01/02/03 Fax: 080-25247724

Ref No: C-DAC/EOI/FPGA Cluster/11-12

July 22, 2011.

Notice Inviting “Expression of Interest” (EOI) to provide a FPGA Cluster based on Xilinx Virtex6 LX550T / Equivalent FPGA at C-DAC Knowledge Park, Bangalore.

Invitation for “Expression of Interest” to provide a FPGA Cluster based on Xilinx Virtex6 LX550T / Equivalent FPGA at C-DAC Knowledge Park, Bangalore

Centre for Development of Advanced Computing (C-DAC), CDAC Knowledge Park, Bangalore invites “Expression of Interest” to provide a FPGA Cluster based on Xilinx Virtex6 LX550T / Equivalent FPGA at C-DAC Knowledge Park, Bangalore from reputed organizations with experience in similar fields.

1.0 ABOUT C-DAC

Over many years of dedicated efforts, C-DAC has become a recognized destination for multi-disciplinary Research Development and deployment of state-of-the-art technologies in Electronics and Information Technology. Today, the broad canvas of C-DAC’s Technology offerings range across several Thematic areas such as High Performance Computing & Grid Computing, Multilingual Computing, Professional Electronics including VLSI & Embedded Systems, Communications and Wireless Technologies, Software Technologies including Open Source Technologies, Cyber Security, Health Informatics, and Education & Training including e-learning.

C-DAC has been in the forefront in the implementation of end-to-end solutions in various verticals of economic and social segments. The technologies have addressed key sectors such as Science and Engineering, Finance, Healthcare, Power, Steel, Agriculture, Cultural Heritage, Industrial controls, Broadcasting & Communications, Education and e-Governance.

2.0 OUR REQUIREMENT

- C-DAC invites “Expression of Interest” (EOI) from reputed firms/organizations to provide a FPGA Cluster based on Xilinx Virtex6 LX550T or Equivalent FPGA.
- The FPGA cluster should be Commercially available Off-The-Shelf (COTS) product
- It should contain at least 21 Virtex6 LX550T -2 speed grade /equivalent FPGAs.
- Each FPGA should have high speed PCI Express / equivalent connectivity to the host machine
- The system should contain at least one PCI-e, one USB (2.0/3.0), one JTAG, one Ethernet (1G or above), SATA port
- High speed interconnection between FPGAs support
- At least one DDR3 SODIMM RAM slot per board
- FPGAs should be configurable through PCIe and JTAG
- The host machine should be loaded with an 64-bit operating system (Windows / Linux)
- Board schematic, software APIs compatible with loaded OS, firmware, IP core support for the board with source code
- Proper enclosure with suitable internal power supply (assume each LX550T/equivalent FPGA consumes 50W of power)

- Proper heat sink & cooling fan arrangements(considering 50W of power consumption by each FPGA) for FPGA / cluster
- The FPGA Cluster should be covered for a minimum of 5 years warranty
- The vendors may be required to provide a minimal system for evaluation purpose.

3.0 Company Profile & Financial Strength

- Organizational: Provide a brief description of the background and organization of your firm/entity. The brief description should include ownership details, date and place of incorporation of the firm, objectives of the firm, availability of appropriate skills among staff etc.,
- Financial: Provide the turnover and net worth of the company on the basis of the audited accounts of the previous three financial years i.e., 2010-11, 2009-10 and 2008-09 with audited statements/annual reports.

4.0 INSTRUCTIONS TO BIDDERS FOR FORMULATION AND SUBMISSION OF PROPOSALS

- The Bidder should be a reputed Original Equipment Manufacturer (OEM) or its authorized Direct Distributor / Dealer. In case of an authorized distributor/dealer an Undertaking, in original, from the OEM is required clearly stating that the Bidder is an authorized dealer of OEM as well as they (OEM) would facilitate the bidder on regular basis with technology / products updates & extend support for the warranty period. In absence of such undertaking, the EOI document is liable to be rejected.
- The instructions mentioned should be read carefully by the Organizations before submitting the bid.
- C-DAC may ask for clarifications or further information to evaluate the Expression of Interests.
- If any information sought in this document is missing or not clearly specified by the bidder, it will be assumed that the Organization is not in a position to supply the information.
- This document is not a Request for Proposal (RFP). After evaluation of the “Expression of Interest, the eligible bidders/vendors will be provided with full set of tender documents and they should furnish their proposal in two part Bid system i.e., Technical bid and Price bid in separate covers. However, the RFP will not be restricted to only those parties who have attended the pre-bid meeting as well as those who have submitted the EOI documents.
- CDAC reserves the right to accept or reject any application without assigning any reasons whatsoever.
- Canvassing in any form would disqualify the bidder.

5.0 PRE-BID MEETING

1. A pre-bid meeting of all the Bidders would be held at C-DAC Knowledge Park Centre, Bangalore on **29-07-2011 at 14.30 hrs.**
2. A maximum of two authorized representatives per Bidder may attend the Pre-bid meeting.

3. Any queries/clarifications may be sent by e-mail to vijendrann@cdacb.ernet.in and aswath@cdacb.ernet.in by the bidders before the date specified under clause 6.0 below.
4. All relevant issues may be raised by Bidders at the Pre-Bid Meeting.
5. All queries raised by the Bidders and clarifications furnished by C-DAC at the Pre-bid Meeting will be documented and made available in our website www.cdacb.in subsequently. However, C-DAC reserves the right to defer answering any such question or to decline to answer any such question, which it considers inappropriate.
6. C-DAC may issue clarifications/amendments/modifications/ and/ or revised version of scope of work/other terms and conditions mentioned in the document and such amendments will be made available in our website www.cdacb.in
7. An undertaking (self certificate) is to be submitted that the Organization has not been blacklisted by any Central/State Government Department/Organization.
8. Please note that all the pages of the EOI documents should be signed with date and seal of the Organization.
9. The covering letter to be submitted by the Bidder along with EOI should be as per the format given in this document on applicant's company Letter Head.

6.0 SCHEDULE FOR SUBMISSION OF EOI:

- | | | | |
|----|---|---|--|
| 1. | Issue of EOI Document | : | 22-07-2011 to 18-08-2011 up to 5:00 pm |
| 2. | Pre-bid Meeting | : | 29-07-2011 at 14.30 hrs |
| 3. | Last Date for submission of queries
(Queries may be submitted to
vijendrann@cdacb.ernet.in /
aswath@cdacb.ernet.in) | : | 05-08-2011 |
| 4. | Last date and time for submission of
completed EOI document along with
documentary evidence as per
"Prequalification - Proforma I" | : | 19-08-2011 at 15.00 hrs |
| 5. | Opening of EOI | : | 19-08-2011 at 15.30 hrs. |

All the pages of EOI document should be duly signed and stamped by the competent authority. The completed EOI document shall be submitted in a sealed envelope super scribing "**Expression of Interest to provide a FPGA Cluster based on Xilinx Virtex6 LX550T / Equivalent FPGA** " on or before the date and time of submission at the following address:

The Head - MMG

Centre for Development of Advanced Computing (C-DAC),
C-DAC Knowledge Park, No.1 Old Madras Road,
Byappanahalli,
Bangalore – 560 038
Phone No.080-66116400/01/02/03
Fax No.080-25247724

The Head (MMG)

Format of Application Form

(On Applicant Company's Letter Head)

To:

The Head (Materials Management Group)
 Centre for Development of Advanced Computing (C-DAC),
 C-DAC Knowledge Park, No.1 Old Madras Road,
 Byappanahalli,
 Bangalore – 560 038

Dear sir,

Sub: Expression of Interest to provide a FPGA Cluster based on Xilinx Virtex6 LX550T / Equivalent FPGA at CDAC, Knowledge Park, Bangalore

This has reference to the advertisement that was published in (give the name of the Newspaper and the date) about providing a FPGA Cluster based on Xilinx Virtex6 LX550T / Equivalent FPGA at CDAC, Knowledge Park, Bangalore. We are interested in submitting our "Expression of Interest" (EOI) for the same.

We would like to state that we qualify for this work as our company meets all the qualifying criteria prescribed by you. The details are as under:

PROFORMA-I

PARTICULARS / DOCUMENTS TO BE FURNISHED ALONG WITH EOI

Name of Organization /Firm/Company	
Year of establishment	
Are you an <ul style="list-style-type: none"> - Original Equipment Manufacturer - Authorized dealer of the OEM? If yes, then submit a copy of Authorization from the manufacturer. 	
Address & Telephone Numbers	
Address & Telephone Numbers of Office/Branch Office in Bangalore. Mention the names and mobile numbers of contact persons with email IDs.	
Status of the firm (Company/ Firm/ Proprietary) enclose the supporting document	
Whether registered with the registrar of companies	

/registrar of firms. If so, mention number and date.	
Turnover of the Company during the last three years (Attach Annual reports)	
a) Name & Mobile Numbers of Directors / Partners / Proprietor	i) ii) iii)
Status and details of disputes/ litigation/ arbitration, if any.	i) ii) iii)
Whether the Organization has been blacklisted by any Central /State Government Department Organization.	
An undertaking (self certificate) is to be submitted that the Organization has not been blacklisted by any Central/State Government Department / Organization.	

We understand that if the details given in support of claims made above are found to be untenable or unverifiable or both, our EOI may be rejected without any reference to us.

Yours sincerely,

Signature with Name & Seal

Place:

Date: